

EXHIBIT I TO PATENT ASSIGNMENT

GDC CONFIDENTIAL

GDC ATM Patents, Patent Applications, and Patents in Process

Attorney File #	Title	US File Date	US File Serial #	US Patent #	US Patent Date
	Synchronizing ATM AAL2 Minicell Generation to Multiple Voice Processing Functions				
GDC-111	SS7 Signalling Gateway Server for Voice Trunking over ATM				
GDC-120	High Speed Switching of Transport Circuits				
GDC-091	Planned Capacity Based Routing for Wide Area ATM Systems				
GDC-097	Load Balancing In ATM Networks				
GDC-098	Priority in ATM Networks				
GDC-089	Voice Transmission Utilizing ATM Protocols				
GDC-126	ATM Network Switch Having Enhanced Call Setup Capability	02/27/98	09/028,585		
GDC-086	ATM Switch with VC Priority Buffers	03/05/98	09/029,295		

Best Available Copy

GDC CONFIDENTIAL

Attorney File #	Title	US File Date	US File Serial #	US Patent #	US Patent Date
	Controlled Available Bit Switch	05/19/98	09/077,219		
GDC-088	Controlled Available Bit Switch				
GDC-118	Hot Insertable UTOP/IA Interface	10/14/98	09/172,545		
GDC-128	A Dynamic Buffer Management for ATM Switches	provisional 7/22/98 final 1/19/01	60/093681 09/744,324		
GDC-129	An Explicit Rate ABR Algorithm for New Generation of ATM Switches	provisional 7/22/98 final 1/19/99	60/093826 09/744,323		
GDC-130	Method for Mapping Narrowband DSO details into AAL type 2 SVCs	04/09/99	09/289,463		
GDC-131	Method & Apparatus for Generation of ATM AAL2 type Broadband Setup Message from Narrowband Setup request	04/09/99	09/289,464		
GDC-132	Transporting Narrowband (voice) Traffic over a Broadband (ATM) Network	08/10/99	09/371,198		
GDC-134	Data Switch	9/29/99	09/408,449		
GDC-135	Bundle Shaping or Policing	09/29/99	PCT/US99/ 22843		

Attorney File #	Title	US File Date	US File Serial #	US Patent #	US Patent Date
GDC-137	Traffic Shaping Mechanism for Packetized Data in a Digital Network	08/10/00	09/636,277		
GDC-071	Allow Access to LMI Address Registration and from SNMP Manager				
GDC-073	Route Encapsulation and Recovery of Node Dependent Paths				
GDC-085	Service Specific Convergence Sublayer for Narrowband Packetization & Transport				
GDC-089	Direct Teletnet Protocol Over SPVCs				
GDC-117	Multicast Connection Label Allocator				
GDC-056	ATM Cell Switch Suitable for Multicast Switching	11/05/93	08/147,939	5,436,883	07/25/95
GDC-057	Data Network Switch	10/04/94	08/317,336	5,461,609	10/24/95
GDC-059	ATM Cell Switch	03/17/95	08/406,300	5,796,956	08/18/98

Attorney File #	Title	US File Date	US File Serial #	US Patent #	US Patent Date
GDC-063	Packet Mode Transfer for Microcell TDM Backplane	09/22/95	08/532,888	5,615,211	03/25/97
GDC-064	Use of OAM Cells across an ATM Frame UNI	09/11/95	08/526,846	5,712,853	01/27/98
GDC-070	User Programmable OAM Loopbacks and Detection in an ATM Network	12/15/95	08/573,244	5,659,540	08/19/97
GDC-072	Routing ATM Switched Virtual Circuit Calls	03/27/96	08/624,812	5,781,529	07/14/98
GDC-075	Proxy Connection End-Points for ATM OAM Functions	07/05/96	08/675,955	5,864,555	01/26/99
GDC-077	Correlation Tag for Billing in ATM	03/29/96	08/622,897	5,949,782	09/07/99
GDC-078	A Traffic Shaping ATM Network Switch	09/15/97	08/913,815	6,044,060	03/28/00
GDC-080	ATM Network Switch with Congestion Level Signaling for Controlling Cell Buffers	05/10/96	08/644,308	5,841,773	11/24/98
GDC-082	ATM Network Switch Congestion Control	12/04/97	08/973,441	6,122,253	09/19/00
GDC-083	Controlling the Flow of ATM Cells in an ATM Network	12/04/97	08/973,442	6,115,358	09/05/00

Attorney File #	Title	US File Date	US File Serial #	US Patent #	US Patent Date
	Method & Apparatus for Connection Admission, BitRate Traffic in an ATM Switch	10/01/97	09/942,021	6,028,840	02/22/00
GDC-084	Dynamic Data Distribution to/from DSP Array (in VSM)	01/29/98	09/015,303	6,041,064	03/21/00
GDC-108	ATM AAL1 & AAL2 Adaptations Using Per Bearer Channel Selectable Data Patch	01/29/98	09/015,302	6,088,365	07/11/00
GDC-109	Fixed-Rate Serial Data Highway for Conveying Variable Rate Data for ATM AAL2 Processing	01/29/98	09/015,301	6,049,544	04/11/00
GDC-110	Precedence & Preemption in ATM Call Admission Control	05/09/97	08/853,654	6,141,322	10/31/00
GDC-139	Coordinating Multiple Processors (ten)				
GDC-140	Efficient Task Sharing between processors (ten)				
GDC-141	Signalling of an Internal Event in an Array of Processors (ten)				
GDC-142	Large Scale Multi-Environment DSP Array (ten)				

GDC CONFIDENTIAL

Attorney File #	Title	UK Date	UK Serial #	US File Date	US File Serial #	US Patent #	US Patent Date
	ATM Network Switch (Semi-Permanent Virtual Paths & Circuits)	10/20/89	2 306 849				
	Virtual Channel Management	05/1/98	2 337 404				
GDC-136	Network Management Information Processing	10/02/98	2 344 262	03/30/01	09/806,783		
GDC-078	Data Network Switch with Fault Tolerance	03/22/00	2 299 914	10/07/97	08/930,973	6,067,286	05/23/00
GDC-133	Communication Network	02/10/98	2 344 029	09/29/99	PCT/US99/22 653		
	Data Switch Performance Monitoring	10/02/98	2 342 527				

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- ☐ **BLACK BORDERS**
- ☐ **IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- ☒ **FADED TEXT OR DRAWING**
- ☐ **BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- ☐ **SKEWED/SLANTED IMAGES**
- ☐ **COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- ☐ **GRAY SCALE DOCUMENTS**
- ☒ **LINES OR MARKS ON ORIGINAL DOCUMENT**
- ☐ **REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- ☐ **OTHER:** _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.